



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P059

#8
M.H.
8-14-03

Inventor(s): Andrew Read, Sameer Halapete and Keith Klayman Group Art Unit:

Filed: 10/23/00

Examiner:

Serial No.: 09/694,433

Title: STATIC POWER CONTROL

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AUG 13 2003

Technology Center 2100

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
6,574,739	DYNAMIC POWER SAVING BY MONITORING CPU UTILIZATION	06/13/03
6,519,706	DSP CONTROL APPARATUS AND METHOD FOR REDUCING POWER CONSUMPTION	02/11/03
6,513,124	METHOD AND APPARATUS FOR CONTROLLING OPERATING SPEED OF PROCESSOR IN COMPUTER	01/28/03
6,510,400	TEMPERATURE CONTROL CIRCUIT FOR CENTRAL PROCESSING UNIT	01/21/03
6,487,668	THERMAL AND POWER MANAGEMENT TO COMPUTER SYSTEMS	11/26/02
6,477,654	MANAGING VT FOR REDUCED POWER USING POWER SETTING COMMANDS IN THE INSTRUCTION STREAM	11/05/02
6,427,211	REAL-TIME POWER CONVERSION AND THERMAL MANAGEMENT FOR ELECTRONIC DEVICES	07/30/02
6,415,388	METHOD AND APPARATUS FOR POWER THROTTLING IN A MICROPROCESSOR USING A CLOSED LOOP FEEDBACK SYSTEM	07/02/02
6,378,081	POWER CONSERVATION WITHOUT PERFORMANCE REDUCTION IN A POWER MANAGED SYSTEM	04/23/02
6,311,287	VARIABLE FREQUENCY CLOCK CONTROL FOR MICRO-PROCESSOR-BASED COMPUTER SYSTEMS	10/30/02
6,141,762	POWER REDUCTION IN A MULTIPROCESSOR DIGITAL SIGNAL PROCESSOR BASED ON PROCESSOR LOAD	10/31/02
6,119,241	SELF REGULATING TEMPERATURE/PERFORMANCE/VOLTAGE SCHEME FOR MICROS (X86)	09/12/00
6,047,248	PERFORMANCE TEMPERATURE OPTIMIZATION BY COOPERATIVE- LY VARYING THE VOLTAGE AND FREQUENCY OF A CIRCUIT	04/04/00

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5,996,084	METHOD AND APPARATUS FOR REAL-TIME CPU THERMAL MANAGEMENT AND POWER CONSERVATION BY ADJUSTING CPU CLOCK FREQUENCY IN ACCORDANCE WITH CPU ACTIVITY	11/30/99
5,996,083	MICROPROCESSOR HAVING SOFTWARE CONTROLLABLE POWER CONSUMPTION	11/30/99
5974,557	METHOD AND SYSTEM FOR PERFORMING THERMAL AND POWER MANAGEMENT FOR A COMPUTER	10/26/99
5,940,786	TEMPERATURE REGULATED CLOCK RATE FOR MICRO-PROCESSORS	08/17/99
5,940,785	PERFORMANCE-TEMPERATURE OPTIMIZATION BY COOPERATIVE- LY VARYING THE VOLTAGE AND FREQUENCY OF A CIRCUIT	08/17/99
5,815,724	METHOD AND APPARATUS FOR CONTROLLING POWER CONSUMPTION IN A MICROPROCESSOR	09/29/98
5,754,869	METHOD AND APPARATUS FOR MANAGING POWER CONSUMPTION OF THE CPU AND ON-BOARD SYSTEM DEVICES OF PERSONAL COMPUTERS	05/19/98
5,719,800	PERFORMANCE THROTTLING TO REDUCE IC POWER CONSUMPTION	02/17/98
5,692,204	METHOD AND APPARATUS FOR COMPUTER SYSTEM POWER MANAGEMENT	11/25/97
5,502,838	TEMPERATURE MANAGEMENT FOR INTEGRATED CIRCUITS	03/26/96
5,461,266	POWER CONSUMPTION CONTROL SYSTEM	10/24/95
5,422,806	TEMPERATURE CONTROL FOR A VARIABLE FREQUENCY CPU	06/06/95

The Examiner's attention is respectfully directed to the following Published Patent Applications:

US. Published Patent Applications

Examiner Initial	No.	Pub. No.	Filing Date	Applicant	Class	Sub-class	Publication Date
	AA	2003/0074591	10/17/01	McClendon et al.	713	322	04/17/03
	BB	2003/0065960	09/28/01	Rusu et al.	713	300	04/17/03
	CC	2002/0083356	11/26/01	Dai	713	322	06/27/02
	DD	2002/0073348	12/06/01	Tani	713	300	06/13/02

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

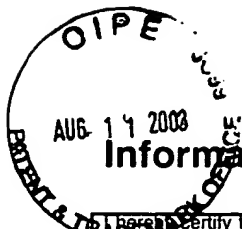
Date: _____

8/5/03

By: _____



Ronald M. Pomeroy
Reg. No. 43,009



Patent

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2185

Docket No.: TRAN-P059

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	08/05/03	Name of Person Making the Deposit:	KATHERINE RINALDI
		Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>

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The Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
..... Informal drawings, totaling sheets.
..... Certification for PTO Consideration
..... Information Disclosure statement (___ sheets)
..... X Information Disclosure statement and late filing fee
..... X Form 1449
..... Petition for Extension of Time
..... X Other: References

Fee Calculation (for other than a small entity)

Fee Items	Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)	\$.00	\$0.00
Information Disclosure Statement, late filing	\$180.00	\$180.00
Other:		\$0.00
Total Fees		\$180.00

PAYMENT OF FEES

- The full fee due in connection with this communication is provided as follows:
 - [X] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - [X] A check in the amount of \$180.00
 - [] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

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By: Ronald M. Pomeroy
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